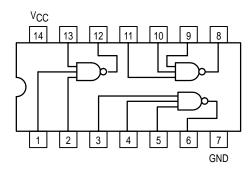


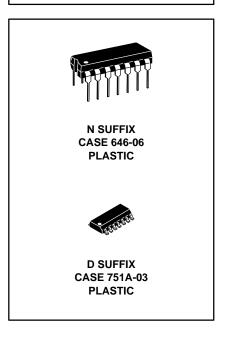
Triple 3-Input NAND Gate

- Outputs Source/Sink 24 mA
- 'ACT10 Has TTL Compatible Inputs



MC74AC10 MC74ACT10

TRIPLE 3-INPUT NAND GATE



MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
V _{in}	DC Input Voltage (Referenced to GND)	-0.5 to V _{CC} +0.5	V
V _{out}	DC Output Voltage (Referenced to GND)	–0.5 to V _{CC} +0.5	V
l _{in}	DC Input Current, per Pin	±20	mA
l _{out}	DC Output Sink/Source Current, per Pin	±50	mA
Icc	DC V _{CC} or GND Current per Output Pin	±50	mA
T _{stg}	Storage Temperature	-65 to +150	°C

^{*} Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter			Тур	Max	Unit	
Vac	Supply Voltage	′AC	2.0	5.0	6.0	V	
Vcc	Зирріў Уонаде	'ACT	4.5	5.0	5.5	V	
V _{in} , V _{out}	DC Input Voltage, Output Voltage (Ref. to GND)		0		Vcc	V	
		V _{CC} @ 3.0 V		150			
1 + +,	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V _{CC} @ 4.5 V		40		ns/V	
		V _{CC} @ 5.5 V		25			
	Input Rise and Fall Time (Note 2)	V _{CC} @ 4.5 V		10		A.	
t _r , t _f	'ACT Devices except Schmitt Inputs	V _{CC} @ 5.5 V		8.0		ns/V	
TJ	Junction Temperature (PDIP)				140	°C	
TA	Operating Ambient Temperature Range		-40	25	85	°C	
loн	Output Current — High				-24	mA	
loL	Output Current — Low				24	mA	

^{1.} V_{in} from 30% to 70% V_{CC} ; see individual Data Sheets for devices that differ from the typical input rise and fall times. 2. V_{in} from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

DC CHARACTERISTICS

	Parameter		74AC VCC (V) TA = +25°C		74AC		
Symbol		V _{CC} (V)			T _A = -40°C to +85°C		Conditions
			Typ Guara		anteed Limits		
VIH	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V
V _{IL}	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V
Vон	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	V	I _{OUT} = -50 μA
		3.0 4.5 5.5		2.56 3.86 4.86	2.46 3.76 4.76	V	*V _{IN} = V _{IL} or V _{IH} -12 mA I _{OH} -24 mA -24 mA
V _{OL}	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1	V	Ι _{ΟΟΤ} = 50 μΑ
		3.0 4.5 5.5		0.36 0.36 0.36	0.44 0.44 0.44	V	*V _{IN} = V _{IL} or V _{IH} 12 mA I _{OL} 24 mA 24 mA
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	±1.0	μΑ	V _I = V _{CC} , GND
lold	†Minimum Dynamic	5.5			75	mA	V _{OLD} = 1.65 V Max
IOHD	Output Current	5.5			- 75	mA	V _{OHD} = 3.85 V Min
ICC	Maximum Quiescent Supply Current	5.5		4.0	40	μΑ	V _{IN} = V _{CC} or GND

^{*} All outputs loaded; thresholds on input associated with output under test.

[†]Maximum test duration 2.0 ms, one output loaded at a time.

Note: I_{IN} and I_{CC} @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V_{CC}.

$\begin{tabular}{ll} \bf AC\ CHARACTERISTICS\ (For\ Figures\ and\ Waveforms\ --\ See\ Section\ 3) \end{tabular}$

			74AC			74AC			
Symbol	Parameter	V _{CC} * (V)		գ = +25° L = 50 p		T _A = - to +8 C _L =		Unit	Fig. No.
			Min	Тур	Max	Min	Max		
^t PLH	Propagation Delay	3.3 5.0	1.5 1.5	6.0 4.5	9.5 7.0	1.0 1.0	10.5 8.0	ns	3-5
^t PHL	Propagation Delay	3.3 5.0	1.5 1.5	5.5 4.0	8.5 6.0	1.0 1.0	10.0 6.5	ns	3-5

 $^{^*}$ Voltage Range 3.3 V is 3.3 V ± 0.3 V. Voltage Range 5.0 V is 5.0 V ± 0.5 V.

DC CHARACTERISTICS

	Parameter		74ACT T _A = +25°C		74ACT			
Symbol		V _{CC}			T _A = -40°C to +85°C	Unit	Conditions	
			Тур	Guar	anteed Limits			
VIH	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V	
V _{IL}	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V	
VOH	Minimum High Level Output Voltage	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V	I _{OUT} = -50 μA	
		4.5 5.5		3.86 4.86	3.76 4.76	V	*V _{IN} = V _{IL} or V _{IH} -24 mA I _{OH} -24 mA	
VOL	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V	Ι _{ΟΟΤ} = 50 μΑ	
		4.5 5.5		0.36 0.36	0.44 0.44	V	*V _{IN} = V _{IL} or V _{IH} 24 mA 1OL 24 mA	
IIN	Maximum Input Leakage Current	5.5		±0.1	±1.0	μΑ	V _I = V _{CC} , GND	
ΔICCT	Additional Max. ICC/Input	5.5	0.6		1.5	mA	$V_{I} = V_{CC} - 2.1 \text{ V}$	
lold	†Minimum Dynamic	5.5			75	mA	V _{OLD} = 1.65 V Max	
IOHD	Output Current	5.5			- 75	mA	V _{OHD} = 3.85 V Min	
ICC	Maximum Quiescent Supply Current	5.5		4.0	40	μΑ	V _{IN} = V _{CC} or GND	

 $^{^{\}star}$ All outputs loaded; thresholds on input associated with output under test. † Maximum test duration 2.0 ms, one output loaded at a time.

AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

			74ACT			74ACT			Fig. No.
Symbol	Parameter	V _{CC} *		C F	T _A = -40°C to +85°C C _L = 50 pF		Unit		
			Min	Тур	Max	Min	Max		
^t PLH	Propagation Delay	5.0	1.0		9.0	1.0	10.0	ns	3-5
tPHL	Propagation Delay	5.0	1.0		9.0	1.0	9.5	ns	3-5

^{*} Voltage Range 5.0 V is 5.0 V \pm 0.5 V.

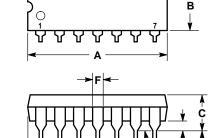
CAPACITANCE

Symbol	Parameter	Value Typ	Unit	Test Conditions
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = 5.0 V
C _{PD}	Power Dissipation Capacitance	25	pF	V _{CC} = 5.0 V

OUTLINE DIMENSIONS

N SUFFIX

PLASTIC DIP PACKAGE CASE 646–06 ISSUE L



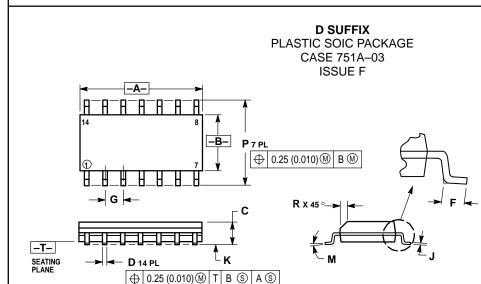


NOTES:

- LEADS WITHIN 0.13 (0.005) RADIUS OF TRUE
 POSITION AT SEATING PLANE AT MAXIMUM
 MATERIAL CONDITION
- DIMENSION L TO CENTER OF LEADS WHEN
 FORMED PARALLEL
- FORMED PARALLEL.

 3. DIMENSION B DOES NOT INCLUDE MOLD
- 4. ROUNDED CORNERS OPTIONAL

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.715	0.770	18.16	19.56	
В	0.240	0.260	6.10	6.60	
С	0.145	0.185	3.69	4.69	
D	0.015	0.021	0.38	0.53	
F	0.040	0.070	1.02	1.78	
G	0.100	BSC	2.54 BSC		
Н	0.052	0.095	1.32	2.41	
J	0.008	0.015	0.20	0.38	
K	0.115	0.135	2.92	3.43	
L	0.300	BSC	7.62 BSC		
М	0°	10°	0°	10°	
N	0.015	0.039	0.39	1.01	



SEATING PLANE

NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
- CONTROLLING DIMENSION: MILLIMETER
 DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006)
 PER SIDE.
- 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES			
DIM	MIN	MAX	MIN	MAX		
Α	8.55	8.75	0.337	0.344		
В	3.80	4.00	0.150	0.157		
С	1.35	1.75	0.054	0.068		
D	0.35	0.49	0.014	0.019		
F	0.40	1.25	0.016	0.049		
G	1.27	BSC	0.050 BSC			
J	0.19	0.25	0.008	0.009		
K	0.10	0.25	0.004	0.009		
M	0 °	7°	0 °	7°		
Р	5.80	6.20	0.228	0.244		
R	0.25	0.50	0.010	0.019		

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